

# Design Automation of Power Electronics: Hardware and Control

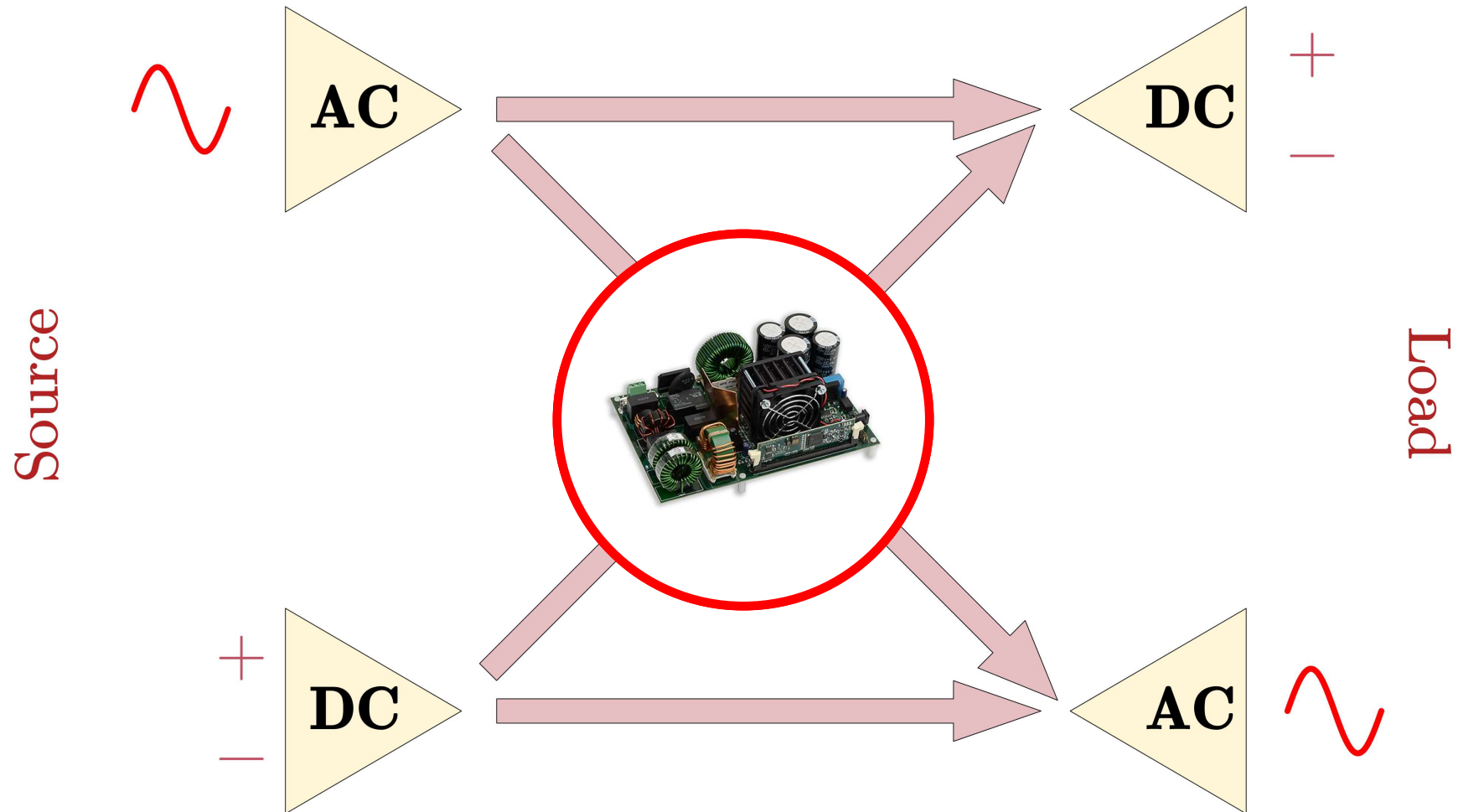
## Presentation to Data-centric Engineering Group

### **Presented by**

Dr. Sinan Li, Lecturer, ARC DECRA Fellow  
School of Electrical and Information Engineering  
University of Sydney (USyd)

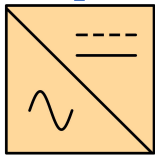


# What is Power Electronics

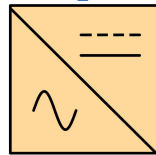


# Ubiquity of Power Electronics

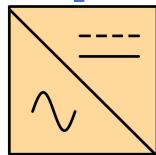
Electric Vehicle



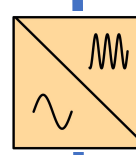
Battery storage



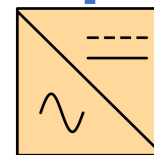
Fuel cell



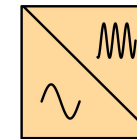
Wind



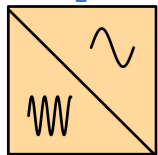
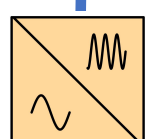
Solar



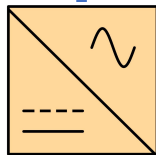
Wireless Charging



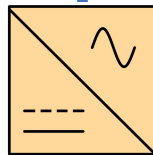
Grid connection



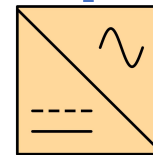
Cooling System



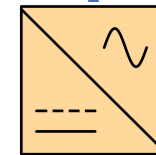
LED



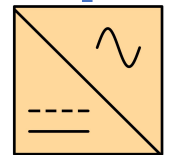
Mobile, Pad, Laptop



HDTV

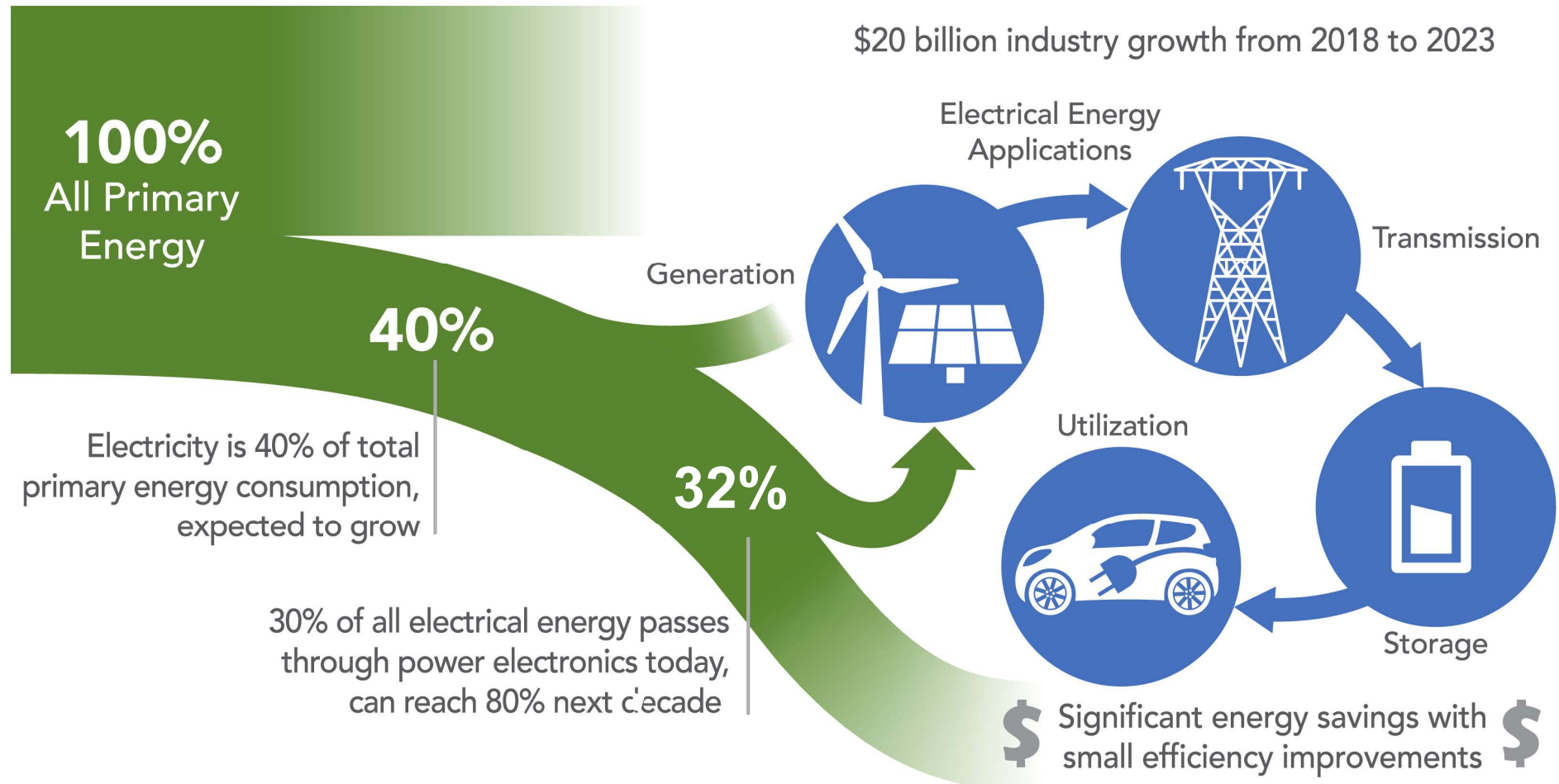


Server



Telecom

# A Future with More Power Electronics [1]



[1] Samantha B. Reese, Timothy Remo, Johnney Green, Andriy Zakutayev, "How Much Will Gallium Oxide Power Electronics Cost?", Joule, Volume 3, Issue 4, 2019, Pages 903-907.

# Opportunities in Design Automation



Design cycle

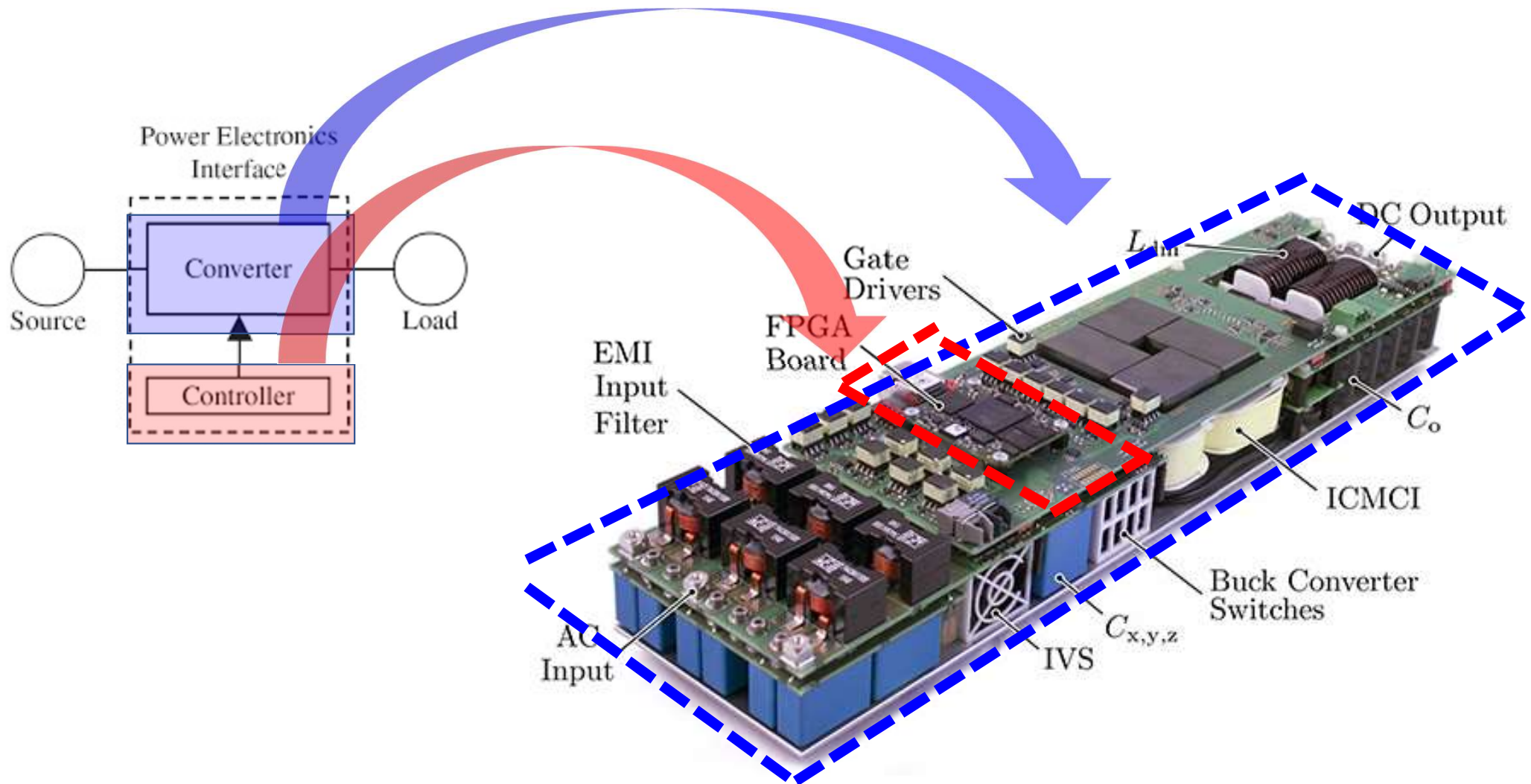
**12–18 months**

Controller testing costs up to

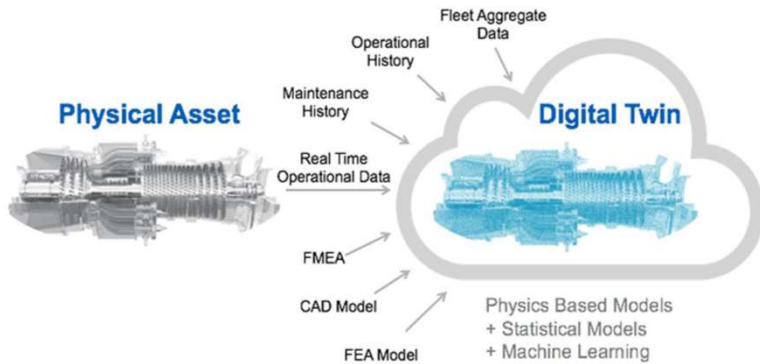
**\$60,000/day**

--- ABB's automotive traction system design [2]

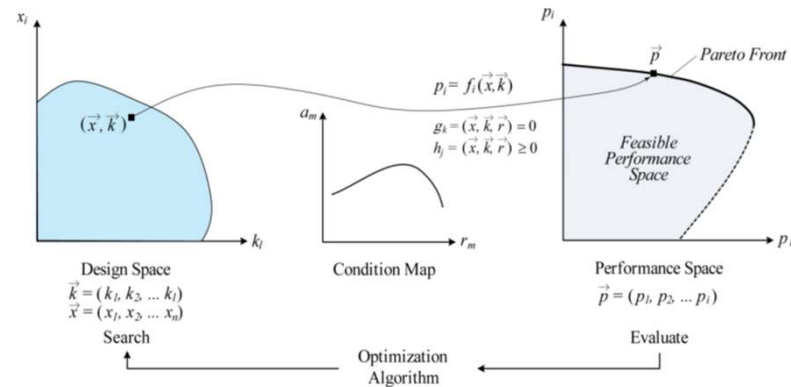
# Overview of My Research Scope



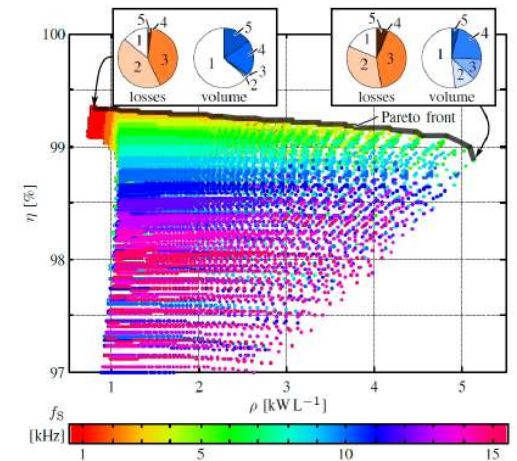
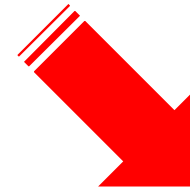
# Topic 1: Design Automation of PE “Hardware”



**Digital Twin Simulation**

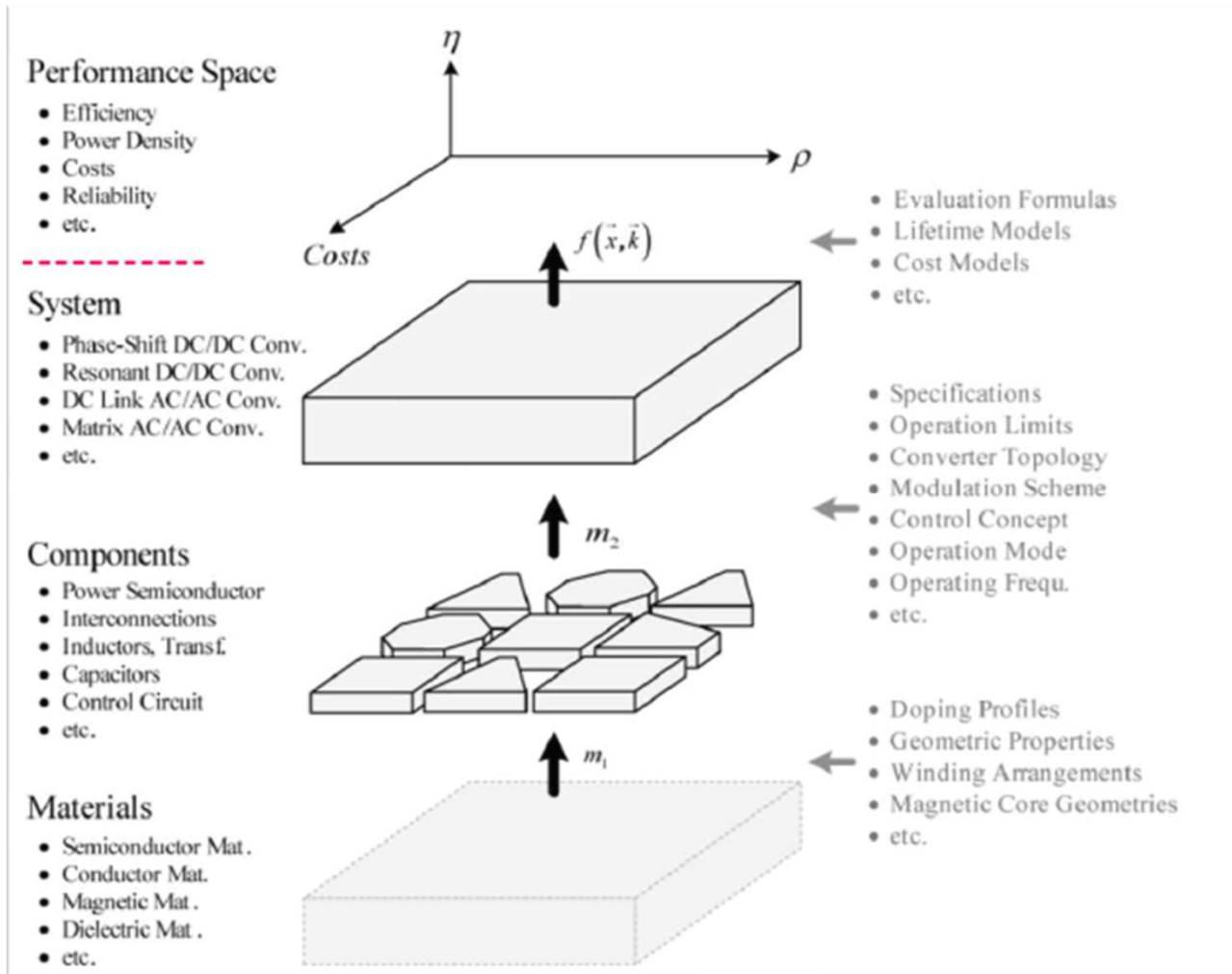


**Performance mapping**



**Optimization**

# Problem: Existing Simulation Techniques are Slow

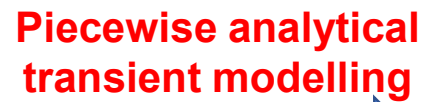


If each simulation takes

**1 second**

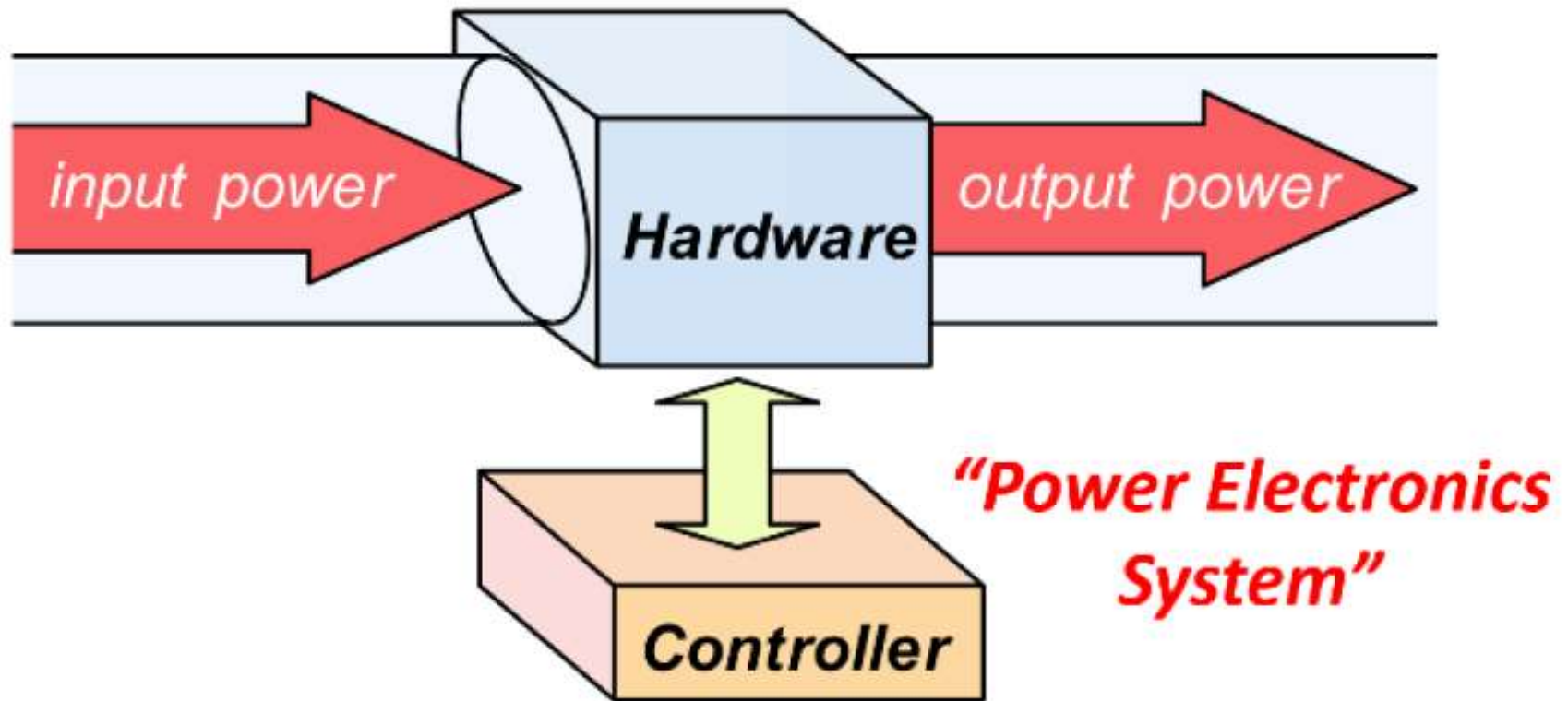
a design space with  $10^8$   
design candidates

needs **> 3 years**  
to complete.

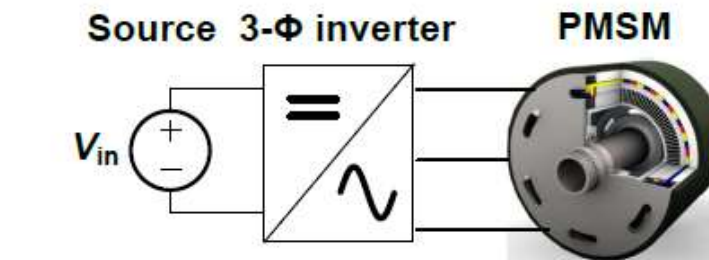


The graph illustrates the turn-on delay characteristics of a MOSFET. The vertical axis represents voltage and current, with key levels marked:  $V_{dc}$ ,  $I_{cmax}$ ,  $I_L$ ,  $V_{Gon}$ ,  $V_{ml}$ ,  $V_T$ ,  $V_{Goff}$ , and 0. The horizontal axis represents time  $t$ . A blue curve shows the collector-emitter voltage  $V_{ce}$  decreasing from  $V_{dc}$  to 0. A green curve shows the gate-emitter voltage  $V_{ge}$  increasing from  $V_{Goff}$  to  $V_{Gon}$ . An orange curve shows the collector current  $i_c$  increasing from 0 to  $I_L$ . A yellow shaded region indicates the turn-on delay, which is the time interval between the start of the gate voltage rise and the point where the collector current reaches  $I_L$ .

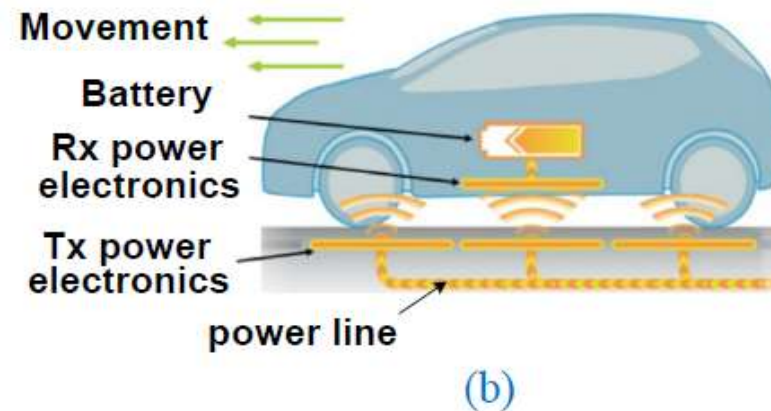
## Topic 2: Design Automation of PE “Controller”



# Project X2 – Model-Free Control



**PE system (1):  
3-Φ PMSM Driving System**



**PE system (2):  
Dynamic WPT System**



THE UNIVERSITY OF SYDNEY

# Thanks + Q&A

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Control**

*Presentation to Data-centric  
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***Sinan Li  
The University of Sydney***

